



PTO/SB/08A (08-03)

Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/618,066		
		Filing Date	July 11, 2003		
		First Named Inventor	Victor Paul Schrader		
		Art Unit	2817		
		Examiner Name	Unassigned		
Sheet	1	of	2	Attorney Docket Number	021343-000300US

U.S. PATENT DOCUMENTS+					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
	AA				
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FOREIGN PATENT DOCUMENTS								
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		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	AW							<input type="checkbox"/>
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	BA							<input type="checkbox"/>
	BB							<input type="checkbox"/>

Examiner Signature	S J Notida	Date Considered	11-04-04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² Kind Codes of U.S. Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.
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PTO/SB/08B (08-03)

Substitute for form 1449B/PTO		INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Complete If Known	
				<i>Application Number</i>	10/618,066
		<i>Filing Date</i>	July 11, 2003		
		<i>First Named Inventor</i>	Victor Paul Schrader		
		<i>Art Unit</i>	2817		
		<i>Examiner Name</i>	Unassigned		
		<i>Attorney Docket Number</i>	021343-000300US		
Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SJ	BC	DENNIS M. MONTICELLI, A Quad CMOS Single-Supply Op Amp with Rail-to-Rail Output Swing, IEEE Journal of Solid-State Circuits, December, 1986, pp1026-1034, Vol. SC-21, No. 6,	
	BD	G. PALMISANO, G. PALUMBO, and R. SALERNO, A 1.5-V High Drive Capability CMOS Op-Amp, IEEE Journal of Solid State Circuits, February, 1999, pp 248-252, Vol. 34, No. 2	
	BE	KA NANG LEUNG, PHILIP K. T. MOK, WING-HUNG KI, JOHNNY K. O. SIN, Three-Stage Large Capacitive Load Amplifier with Damping-Factor-Control Frequency Compensation, IEEE Transactions on Solid-State Circuits, February, 2000, pp. 221 - 230, Vol. 35, No. 2	
	BF		
	BG		
	BH		
	BI		
	BJ		
	BK		
	BL		
	BM		

Examiner Signature	S J Mottola	Date Considered	11-04-04
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